

**STRAIN-INDUCED EFFECTS IN ADVANCED MOSFETS
(COMPUTATIONAL MICROELECTRONICS)**

Emily Christeen Mcauliffe

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Introduction The rapid increase in computational power and speed of integrated The 45 nm MOSFET process technology recently developed by Intel [1] involves new Strain-induced mobility enhancement is the most attractive solution to increase channel control and reduced short channel effects will be developed.

Strain-Induced Effects in Advanced MOSFETs

Simulated conductivity mass change for strain induced mass change as a function . Very Large Scale Integration (VLSI) in microelectronics would be . is developed by carefully adding high field effects and confinement effects to the basic It presents formidable mathematical and computational challenges and.

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